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February 29, 2008

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Certificate
MAR 12 2008
of Correction

Re: **CERTIFICATE OF CORRECTION**
U. S. Letters Patent No. 7,174,418 B2
Issued: Feb. 6, 2007
For: Semiconductor Memory Device for Enhancing Refresh Operation In
High Speed Data Access
Inventor: Hong et al.
Our File No. 051876.P557

Dear Sir:

Enclosed is a Certificate of Correction (two copies) for the above-referenced patent.

This request for correction is made under rule 322 of the Rules of Practice and 35 U.S.C. Section 254.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Eric S. Hyman
Reg. No. 30,139

ESH/ms
enclosures
PTO Form- 1449

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Patent Publication

MAR 12 2008

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO: 7,174,418 B2

DATED: Feb. 6 2007

INVENTOR(S): Hong et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In Claim #2, line 1, please delete "claim 1 wherein" and insert - - claim 1, wherein - -.

MAILING ADDRESS OF SENDER:
BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP
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PATENT NO. 7,174,418 B2

Certificate of Correction (PTO Form 1050)-Amended

1 of 1

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